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INFORMATION DISCLOSURE STATEMENT

Applicants: Akira MASE

Filing Date: November 29, 2001

Group Art Unit:

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09/99/866

11/26/01

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
<i>al</i>	3,079,282 ✓	02/01/1963	Haller et al.			
	3,621,564 ✓	11/01/1971	Tanaka et al.			
	3,822,467 ...	07/09/1974	Symersky			
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	4,145,120	03/20/1979	Kubota			
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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
	0 154 443	09/11/1985	EP			Full Eng
	2 032 127	04/30/1980	UK			Full Eng
<i>al</i>	2 064 233	06/01/1981	UK			Full Eng
<i>a</i>	2 160 693	12/01/1985	UK			Full Eng
	2 182 209	04/27/1973	FR			Conc Stmt
	2 525 209	10/21/1983	FR			Conc Stmt
	51-102466	09/09/1976	JP			Conc Stmt
<i>al</i>	53-092465	08/01/1978	JP			
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	58-148434	09/01/1983	JP			

Examiner

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
<i>a</i>	60-092648	05/01/1985	JP				
	60-107845	06/13/1985	JP			Eng Abst	
	60-108822	06/14/1985	JP			Eng Abst	
	60-180151	09/01/1985	JP				
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OTHER DOCUMENT (Including Author, Title, Date, Pertinent Pages, Etc.)

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<i>a</i>	IBM Technical Disclosure Bulletin, February 1966, Vol. 8, No. 9
<i>a</i>	Mitchell et al., "Processing Techniques for Fabricating Thick Film Copper/Dielectric Multilayer Structures", pp. 1-10, May 14-16, 1979, IEEE 29th Electronic Components Conference

Examiner

Hoan Nguyen

Date Considered

5/23/03

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